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(57) Abstract:

**PROBLEM TO BE SOLVED:** To allow a digital signal processor to digital-process the DC offset of a received signal by disconnecting a signal route to a receiver from an antenna by a switch just before the receiving timing of a time division multiple access system and executing DC offset detection at the point of the time.

**SOLUTION:** A switch 102a is opened to disconnect a signal input to the receiver from a transmission-reception antenna 101 just before the receiving timing. At the same timing a switch 102c is turned on to ground the input of a receiving amplifier 103. In this state, each digital value of the I, Q outputs of an orthogonal wave detector 110 is stored within a decoder 115. In succession, the switch 102a is turned on with the receiving timing and the switch 102c is opened to input the received signal from the antenna 101 to the amplifier 103 and at the time of decoding by the decoder 115, the signal is corrected by using a stored DC offset component.

